Morello Supplement to the Arm C Language Extensions

Release ACLE Q3 2020

Arm Limited or its affiliates.

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Contents

1	Prea	mble 2				
	1.1	Abstract				
	1.2	Keywords				
	1.3	Non-Confidential Proprietary Notice				
	1.4	Confidentiality status				
	1.5	Product Status				
2	Abou	at This Document 4				
	2.1	Change Control				
	2.2	References				
	2.3	Terms & Abbreviations 5				
3	Scop	e 6				
4	Pred	Predefined macros				
	4.1	ARM_FEATURE_C64				
	4.2	Capability Permissions				
	4.3	Deviation from CHERI				
5 Builtin functions						
	5.1	Check subset and conditionally unseal or return null				
	5.2	Check subset and conditionally unseal				
	5.3	Convert pointer to capability offset (zeroing form) 8				
In	dex	9				

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Preamble

1.1 Abstract

This document specifies the Arm C Language Extensions to enable C/C++ programmers to exploit the Morello architecture with minimal restrictions on source code portability.

1.2 Keywords

Predefined macros, builtin functions

1.3 Non-Confidential Proprietary Notice

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Web Address http://www.arm.com

About This Document

2.1 Change Control

2.1.1 Current Status and Anticipated Changes

The following support level definitions are used by the ACLE specifications:

- **Release** Arm considers this specification to have enough implementations, which have received sufficient testing, to verify that it is correct. The details of these criteria are dependent on the scale and complexity of the change over previous versions: small, simple changes might only require one implementation, but more complex changes require multiple independent implementations, which have been rigorously tested for cross-compatibility. Arm anticipates that future changes to this specification will be limited to typographical corrections, clarifications and compatible extensions.
- **Beta** Arm considers this specification to be complete, but existing implementations do not meet the requirements for confidence in its release quality. Arm may need to make incompatible changes if issues emerge from its implementation.
- Alpha The content of this specification is a draft, and Arm considers the likelihood of future incompatible changes to be significant.

All content in this document is at the Alpha quality level.

2.1.2 Change History

Issue	Date	Change
00alpha	30th September 2020	Alpha release

2.2 References

This document refers to, or is referred to by, the following documents.

Ref	URL or other reference	Title
ACLE-	This document	Morello Supplement to the Arm C Language
morello		Extensions
ACLE	Document number: 101028	Arm C Language Extensions
CHERI	https://www.cl.cam.ac.uk/techreports/	CHERI C/C++ Programming Guide
	UCAM-CL-TR-947.pdf	

2.3 Terms & Abbreviations

- **Capability** The capability data type is an unforgeable token of authority which provides a foundation for fine grained memory protection and strong compartmentalisation.
- **Permissions** The permissions mask controls how the capability can be used for example, by authorizing the loading and storing of data and/or capabilities.
- **Deriving a capability** A capability value CV2 is said to be derived from a capability value CV1 when CV2 is a copy of CV1 with optionally removed permissions and/or optionally narrowed bounds (base increased or limit reduced).
- **Sealing a capability** When a capability is sealed it cannot be modified or dereferenced, but it can be used to implement opaque pointer types.

Scope

The Morello Supplement to the Arm C Language Extensions highlights the language features added on top of the CHERI programming language to further exploit the Morello architecture. We strongly recommend reading the CHERI Pure-Capability C/C++ Programming Guide as preliminary material: https://www.cl.cam.ac.uk/techreports/UCAM-CL-TR-947.pdf

Predefined macros

ACLE introduces several predefined macros that define how the C/C++ implementation uses the Morello architecture.

4.1 __ARM_FEATURE_C64

This macro indicates that the code is being compiled for the C64 ISA.

4.2 Capability Permissions

The following macros indicate capability permissions:

Name	Value
ARM_CAP_PERMISSION_EXECUTIVE	2
ARM_CAP_PERMISSION_MUTABLE_LOAD	64
ARM_CAP_PERMISSION_COMPARTMENT_ID	128
ARM_CAP_PERMISSION_BRANCH_SEALED_PAIR	256

Those can be used to form a bitmask that is acceptable for cheri_perms_and() and cheri_perms_clear(). The value of each macro corresponds to the permission bit as it appears in the architecture documentation.

4.3 Deviation from CHERI

The macro ____CHERI_CAP_PERMISSION_PERMIT_CCALL___ is not available on the Morello architecture.

Builtin functions

ACLE standardizes builtin functions to access the Morello architecture. These are the following:

5.1 Check subset and conditionally unseal or return null

Assuming two valid capabilities a and b, with the former being sealed and the latter being unsealed, if a can be derived from b, then it unseals a and returns it, otherwise it returns a null capability.

5.2 Check subset and conditionally unseal

Assuming two valid capabilities a and b, with the former being sealed and the latter being unsealed, if a can be derived from b, then it unseals a and returns it, otherwise it just returns a.

5.3 Convert pointer to capability offset (zeroing form)

```
void* __capability
__builtin_morello_cvtz(const void* __capability a, size_t b)
```

If the specified offset b is zero, then it returns a null capability, otherwise it sets the offset of capability a to b and returns a. If capability a is sealed then the returned capability is marked invalid.

Index

С

Capability,5

D

Deriving a capability, **5**

Ρ

Permissions, 5

S

Sealing a capability, ${\bf 5}$